

Roll No.
800 -/25/

February 2019
M. Sc. IIIrd Semester Examination

PHYSICS

Third Paper : Digital Electronics

Time 3 Hours]

[Max. Marks : Regular 85 / Private 100

[Min. Marks : Regular 28 / Private 33

Note : This question paper is meant for all Regular and Private students. Answer all five questions. All questions carry equal marks. The blind candidates will be given 60 minutes extra time.

1. Answer any four of the following :
 - (a) Convert the decimal number 2479 into hexadecimal number $(2479)_{10} = (?)_{16}$.
 - (b) $(56)_{10} = (?)_8$
 - (c) $(7428)_8 = (?)_2$
 - (d) Subtract $(24)_{16}$ from $(84)_{16}$?
 - (e) Add $(1A8)_{16}$ and $(67B)_{16}$?
2. Explain any four of the following :
 - (a) XOR Gate
 - (b) XNOR Gate
 - (c) ASCII Code
 - (d) EBCDIC Code
 - (e) Demorgans Theorem
 - (f) Half Adder
 - (g) Full Adder.
3. What is shift Register ? Explain left and right shift register with clock diagram.
OR
 - (a) Explain D flip-flop.
 - (b) What is Encoder ? Explain decimal to BCD encoder.
4. What is Ripple Counter ? Explain into circuit diagram with J-K flip-flop.
OR
What is Ring Counter ? Explain its circuit diagram with D flip-flop.
5. Write the short notes on any three of the following :
 - (a) Weighted resistor D/A convertor.
 - (b) R-2R ladder D/A convertor.
 - (c) Counter method A/D convertor.
 - (d) Integrating ADC.

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